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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/762,323	01/23/2004	Taku Nakamura	58799-104	4967

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EXAMINER	
RAO, ANAND SHASHIKANT	
ART UNIT	PAPER NUMBER
2621	

MAIL DATE	DELIVERY MODE
12/06/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No.	Applicant(s)	
	10/762,323	NAKAMURA ET AL.	
	Examiner	Art Unit	
	Andy S. Rao	2621	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☐ Responsive to communication(s) filed on _____.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *10-25* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☐ Claim(s) _____ is/are rejected.

7)☒ Claim(s) *10-25* is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☒ All b)☐ Some * c)☐ None of:

1.☒ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☐ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☒ Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date *12/2/05*.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____.
5) ☐ Notice of Informal Patent Application
6) ☐ Other: _____.

DETAILED ACTION

Specification

1. The lengthy specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 10-25 are rejected under 35 U.S.C. 102(e) as being anticipated by Post et al., (hereinafter referred to as "Post")

Post discloses a digital broadcast receiver comprising: an input terminal that receives a compressed input signal including a data signal (Post: column 3, lines 40-45); a demultiplexer that separates data from the input signal (Post: column 3, lines 45-50); a decoder buffer for that stores a compressed video signal (Post: figure 1B, element 156) and a compressed audio signal (Post: figure 1B, element 158); a video decoder that decodes the compressed video signal stored in the decoder buffer (Post: figure 1B, element 160); an audio decoder for decoding said compressed audio signal stored in the decoder buffer (Post: figure 1B, element 162); a memory for storing said data signal separated by the demultiplexer (Post: figure 1A, element 104); and a

CPU for analyzing the data signal stored in said memory (Post: column 3, lines 7-12); wherein the CPU allows the data signal analyzed by the CPU to be stored in the decoder buffer when the analyzed data signal includes a compressed video signal or a compressed audio signal, and the compression method used for the compressed video signal or the compressed audio signal in the data signal can be decoded by the video decoder or the audio decoder (Post: column 4, lines 1-10 and 41-51), as in claim 10.

Regarding claim 11, Post discloses that the CPU decodes analyzed data if the compression method used for the compression video signal or the compressed audio signal included in the data signal cannot be decoded by the video decoder or the audio decoder (Post: column 3, lines 55-65), as in the claim.

Regarding claim 12, Post discloses using time division (Post: column 5, lines 50-60), as in the claim.

Regarding claim 13, Post discloses using write and read addresses to determine the depletion of the audio and video buffers (Post: column 5, lines 1-34), as in the claims.

Regarding claims 14-15, Post discloses using interrupts signals as in the claims (Post: column 6, lines 1-10), as in the claims.

Post discloses a digital broadcast receiver comprising: an input terminal that receives a compressed input signal including a data signal (Post: column 3, lines 40-45); a demultiplexer for separating a data signal from the input signal (Post: column 3, lines 45-50); a decoder buffer for storing a compressed video signal (Post: figure 1B, element 156) and a compressed audio signal (Post: figure 1B, element 158); a video decoder for decoding said compressed video signal in said decoder buffer (Post: figure 1B, element 160); a display for displaying a video signal

decoded by said video decoder (Post: column 3, lines 63-68); an audio decoder for decoding said compressed audio signal in said decoder buffer (Post: figure 1B, element 162); a speaker for outputting an audio signal decoded by said audio decoder (Post: column 3, lines 18-20); a memory for storing said data signal separated by said demultiplexer (Post: figure 1A, element 104); and a CPU for analyzing the data signal stored in said memory (Post: column 3, lines 7-12); wherein said CPU allows the data signal analyzed by said CPU to be stored in said decoder buffer if the analyzed data signal includes a compressed video signal or a compressed audio signal, and the compression method used for the compressed video signal or the compressed audio signal included in the data signal can be decoded by the video decoder or the audio decoder (Post: column 4, lines 1-10 and 41-51), as in claim 16.

Post discloses a CPU (Post: column 3, lines 7-12) for receiving and processing an input signal including a data signal (Post: column 3, lines 40-45), wherein: said CPU determines whether a compressed video signal of a compressed audio signal is included in said data signal (Post: column 3, lines 45-50), and determines and the compression method used for the compressed video signal or the compressed audio signal in the data signal (Post: column 4, lines 1-10 and 50-55), and based on the result changes to destination to which the data signal is outputted (Post: column 3, lines 57-62), as in claim 17.

Regarding claim 18, Post discloses wherein: if the CPU determines that that the decompression method used for a compressed video signal or a compressed audio signal included in the data signal can be decoded by a video decoder or a audio decoder (Post: column 4, lines 10-30), the CPU causes the data signal to be stored in a decoder buffer to be decoded by the video decoder or the audio decoder (Post: column 7, lines 1-20), and if the CPU determines

that the compression method used for the compressed video signal or the compressed audio signal included in the data signal cannot be decoded by the video decoder or the audio decoder, the CPU decodes the data signal (Post: column 7, lines 30-50), as in the claim.

Post discloses a decoder for receiving and processing a compressed input signal including a data signal (Post: column 3, lines 45-50), comprising: a decoder buffer for storing the compressed video signal (Post: figure 1B, element 156) and the compressed audio signal (Post: figure 1B, element 158); a video decoder for decoding said compressed video signal in said decoder buffer (Post: figure 1B, element 160); an audio decoder for decoding said compressed audio signal in said decoder buffer (Post: figure 1B, element 162); and a CPU for analyzing said data signal (Post: column 3, lines 7-12), wherein said CPU allows the data signal analyzed by said CPU to be stored in said decoder buffer if the analyzed data signal includes a compressed video signal or a compressed audio signal, and the compression method used for the compressed video signal or the compressed audio signal included in the data signal can be decoded by the video decoder or the audio decoder (Post: column 4, lines 1-10 and 41-51), as in claim 19.

Post discloses a signal receiver comprising: a demultiplexer configured to receive a compressed input signal and separate a data signal from an input signal (Post: column 3, lines 45-50); a decoder buffer coupled to the demultiplexer to store a compressed video signal (Post: figure 1B, element 156) and a compressed audio signal (Post: figure 1B, element 158); a video decoder coupled to the decoder buffer to receive and decode the compressed video signal (Post: figure 1B, element 160); an audio decoder coupled to the decoder buffer to receive and decode the compressed audio signal (Post: figure 1B, element 162); a memory coupled to the demultiplexer to receive and store the data signal (Post: figure 1A, element 104); a CPU coupled

to the memory to receive and analyze the data signal stored in the memory (Post: column 3, lines 7-12); wherein if the data signal includes a compressed video signal or a compressed audio signal that is compressed using a compression method that can be decoded by the video decoder or the audio decoder (Post: column 4, lines 1-10 and 41-51), the CPU causes the data signal to be stored in the decoder buffer to enable processing of the data signal by at least one of the decoders (Post: column 5, lines 1-20), as in claim 20.

Regarding claim 21, Post discloses that the CPU decodes analyzed data if the compression method used for the compression video signal or the compressed audio signal included in the data signal cannot be decoded by the video decoder or the audio decoder (Post: column 3, lines 55-65), as in the claim.

Regarding claim 22, Post discloses using time division (Post: column 5, lines 50-60), as in the claim.

Regarding claim 23, Post discloses using write and read addresses to determine the depletion of the audio and video buffers (Post: column 5, lines 1-34), as in the claims.

Regarding claims 24-25, Post discloses using interrupts signals as in the claims (Post: column 6, lines 1-10), as in the claims.

Conclusion

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Andy S. Rao whose telephone number is (571)-272-7337. The examiner can normally be reached on Monday-Friday 8 hours.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mehrdad Dastouri can be reached on (571)-272-7418. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Andy S. Rao
Primary Examiner
Art Unit 2621

asr
December 5, 2007